responses to the CPU core 202 is not an issue. This is one advantage of the testing methodology illustrated in Figure 2 over the testing methodology illustrated in Figure 1. However, the methodology illustrated with respect to Figure 2 suffers from a disadvantage in that the IO mechanisms are not tested. --

Please replace Paragraph [0017] on Page 7 in the section entitled "Detailed Description" with the following paragraph:

-- [0017] The steps outlined in the flowchart of Figure 3 [[is]] are best understood with reference to Figure 5. --

Please replace Paragraph [0017] on Page 7 in the section entitled "Detailed Description" with the following paragraph:

Please replace the paragraph on Page 21 under the heading entitled "Abstract" with the following paragraph:

-- A method of testing a DUT is provided. The method comprises loading a memory within a link-based system with a functional test program, executing the functional test program in a processor core of the link-based system, and routing test signals generated during execution of the functional test program to a response agent embedded in the link-based system via an external path. --